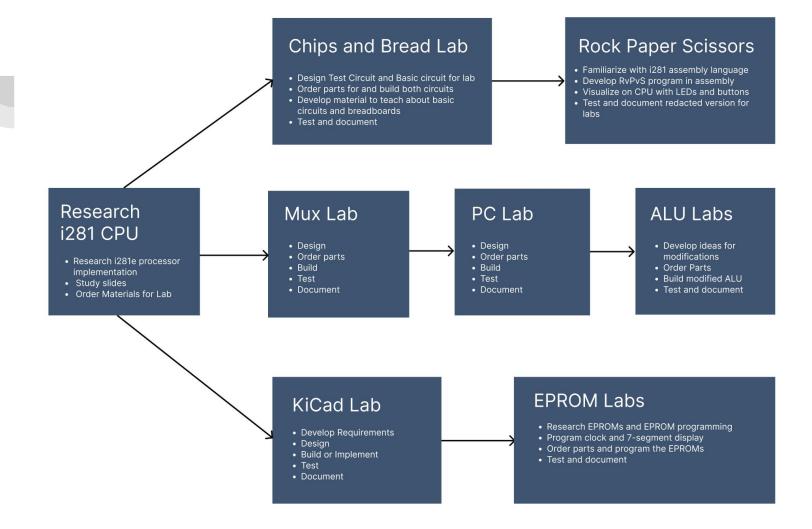
i281 CPU

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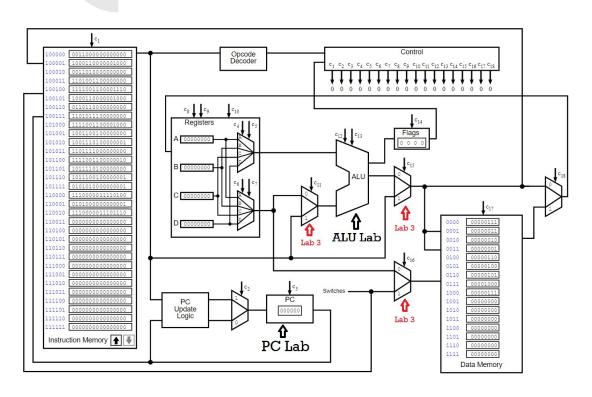
Project Overview

- Utilize the existing i281e CPU designed by previous senior design teams.
- Assemble parts for another microprocessor and document the process.
- Design, test, and document 10 lab activities for a new class.
- Create and implement several outreach activities.
- Problem Statement Design and implement a set of labs and activities based around the i281e CPU





Detailed Design and Visuals



Lab 1: Implementing Digital Logic

Lab 3: Multiplexer

Lab 5: Program Counter

Lab 8: Registers

Lab 9: ALU

Functionality

- The i281e processor is intended for students who what to fill the knowledge gap after taking CPRE 281, 288 and 381
- The user is required to complete labs
- The system would respond by outputting LEDs or other testing methods to check that our labs work

Technology Considerations

Hardware

Chips:

- Some chips used in existing design are no longer produced so we needed to find a comparable solution
- The chip we decided on the AT28C256

Wiring:

- The lab room for the class does not allow for cutting wires
- Wire kits have limited lengths and colors
- The PC lab will require a lot of wires, more colors makes the circuits easier to follow

Simulator

- Need visual representations of for the labs with either photos or a simulator
- Needed to keep in mind cost, component availability, and ease of use

Currently using TinkerCAD since it's free, has a wide range of components and neatly depicts breadboards.

Areas of Concern and Development

- The current design can satisfy the needs due to supplementing our labs to further the user's education
- One of our concerns is that our labs may be a bit too advanced for some users, like middle schoolers, in the sense of delivering the lab material and ending up with a lower than expected success rate for the labs.

Conclusion

- Our labs will be sufficient to deliver our students during outreach events
- Our labs consist of a mix of digital logic focused labs and hardware focused labs
- Some constraints may be due to measuring lab success rate

Questions?